

Applic. No. 10/075,670
Amdt. dated December 30, 2005
Reply to Office action of September 30, 2005

Specification Amendments

Replace the paragraph between lines 5-23 on page 10 with the following:

--Referring now to the figures of the drawing in detail and first, particularly, to Fig. 1 thereof, there is shown a raster generation system that includes a preprocessing unit 1. The preprocessing unit 1 is connected, via a network 3, to a raster processor 5 (RIP) for processing the raster data. A raster memory 7 with random access (RAM) is provided for the raster data, and a DMA (Direct Memory Access) controller 9 is provided for controlling the process of outputting the raster data to the buffer memory 11. The buffer memory 11 is connected to image-setting electronics 13 belonging to an image-setting unit having numerous laser diodes 15. The raster processor 5 with its CPU and the RAM raster memory 7 are arranged on a first plug-in interface board 17 and are connected via a standard bus, for example, a compact PCI (Peripheral Component Interface) or VME (Versa Module Europa) system, to a second plug-in interface board 19. The DMA controller 9, the buffer memory 11 and, if appropriate, the image-setting electronics 13 or laser drive system are arranged on the second plug-in interface board 19.--